

ABSTRACT OF THE DISCLOSURE

When a burst data transfer is started and a data phase reaches an address to which data is not to be transferred, a byte enable signal is deasserted. When the data phase reaches an address to which data is to be transferred, the byte enable signal is asserted, and
5 data to be transferred is updated only at the time when this data phase is completed. With such an arrangement, the data can be transferred only to destination addresses which are separated with intervals of several words.